

LA7938

Electronic Channel Select System Control Circuit for TV / VCR Use

Overview

The Sanyo LA7938 Monolithic Linear TV/VCR Electronic Tuner System Controller IC integrates all the peripheral circuitry for a TV or VTR tuner, with the exception of the microcontroller, into a single chip.

It incorporates a 2-input/4-output band-switch, 5.0V and 5.75V voltage regulators, comparator, sync signal processing circuit, AFT DC shift circuit and constant current circuit. Each PNP output of the band-switch circuit typically sources 40mA, eliminating the need for external current drivers.

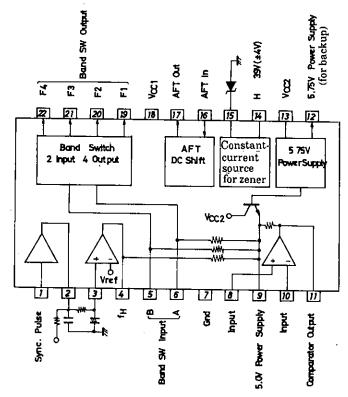
The LA7938 operates from a recommended supply voltage range of 8.7 to 12.5V. It is available in 22-pin shrink DIPs.

Features

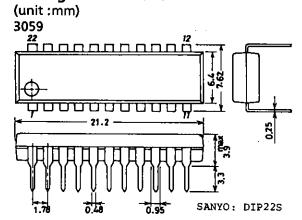
- · Integrates all tuner peripheral circuits except controller
- · Band-switch outputs source up to 40mA
- · Regulators each supply up to 50mA
- · 22-pin shrink DIP

Block Diagram

[For backup purposes]



Package Dimensions



Maximum Ratings at Ta = 25°C				unit	
Allowable Power Dissipation	Pd max	Ta≦65°C	1000	mW	
Operating Temperature	\mathbf{Topr}		-20 to +65	°C /	
Storage Tempeature	Tstg		-55 to +150	°C	
[Band-Switch]					
V _{CC1} Maximum Supply Voltage	e V ₁₈ max		13	V	
Maximum Load Current	I ₁₉ max		-5 0	mA	
	I ₂₀ max		-50	mΑ	
	I ₂₁ max		- 50	mΑ	
	I ₂₂ max		-50	mΑ	
Applied Imput Voltage	V ₆ max		3.5	V	
	$ m V_{5}max$		3.5	V	
[Sync Detector]					
Positive Input Voltage	$V_1 \max$		3.5	V	
Negative Input Voltage	$-V_1$ max	K	-1.4	V	
Applied Input Voltage (Pin3)	V_3 max	$V_{CC}1 = 13V$	10	V	
Applied Input Voltage	V_4 max	$V_{\rm CC}1=V_{\rm CC}2=12V$	4.6	V	
[Voltage Regulators]					
V _{CC2} Supply Voltage	V ₁₃ max		13	V	
+5.75V Output Current	I_{12} max		-50*	mΑ	
+5.0V Output Current	I ₉ max		-50*	mA	
[Comparator]					
Maximum Input Voltage		$V_{\rm CC}2=13V$	13	V	
		$V_{\rm CC}2=13V$	13	V	
Applied Output Voltage	V ₁₁ max		6	V	
[+31V Constant Current Source					
Applied Voltage	V ₁₄ max		43	V	
[AFT Shift Circuit]					
Maximum Input Voltage	V ₁₆ max	$V_{\rm CC}1=13V$	13	V	

^{*}: The rating for the total current drawn from both the 5.0V and 5.75V supplies is 70 mA.

Operating Conditions at Ta = 25°	C.
Operating Voltage Range	V _{CC} op

unit

8.7 to 12.5

(Band-Switch Truth Table)

Input			Output			
A (Pin6)	B (Pin5)	F1 (Pin19)	F2 (Pin20)	F3 (Pin21)	F4 (Pin22)	
L	L	Н	Z	Z	Z	
Н	L	Z	Н	Z	Z	
L	Н	Z	Z	Н	Z	
Н	Н	Z	Z	Z	Н	

Z: HIGH-impedance

Quiescent Current IcCl	Operating Characteristics at T		$_{\rm CC}$ 1, $_{\rm VCC}$ 2=12 $_{\rm V}$	min	typ	max	unit
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$		1CC1			9.0		mA
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	•	Icc2			7.0		mΔ
$ [Band-Switch] \\ Output Saturation Voltage \\ F_2 (sat.) \\ F_3 (sat.) \\ F_3 (sat.) \\ F_4 (sat.) \\ F_2 (sat.) \\ F_3 (sat.) \\ F_3 (sat.) \\ F_4 (sat.) \\ F_2 (sat.) \\ F_3 (sat.) \\ F_4 (sat.) \\ F_3 (sat.) \\ F_4 (sat.) \\ F_4 (sat.) \\ F_2 (sat.) \\ F_3 (sat.) \\ F_3 (sat.) \\ F_4 (sat.) \\ F_3 (sat.) \\ F_4 (sat.) \\ F_3 (sat.) \\ F_4 (sat.)$	=	1002			1.0		шл
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$,
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$		F ₁ (sat.)	$I_0 = -40 \text{mA}$			0.7	V
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$			_				
H-Level Input Voltage V_{5HI} Open gate type microcomputer must be in OFF state (pull-up resistance on chip). L-Level Input Voltage V_{5LO} 0.8 V 0.9 V 0.9 U 0.9 V 0.9 U 0.9 V 0.9 V 0.9 U 0.9 V 0.9 U 0.9 V 0.9 U 0.9		F ₃ (sat.)	$I_0 = -40 \text{mA}$			0.7	v
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$		F_4 (sat.)	$I_{O} = -40 \text{mA}$			0.7	V
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	H-Level Input Voltage	V_{5HI}	Open gate type microcomputer				
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$		V_{6HI}					
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$			(pull-up resistance on chip).				
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	L-Level Input Voltage						V
$ [Sync Circuit] \\ Input Threshold Voltage & V_{1TH} & 0.4 & 0.72 & 1.5 & V \\ Pin2 Output & V_{2(sat.)} & I_{SINK} = 10 mA & 1.0 & V \\ Saturation Voltage & & & & & & & & \\ Pin3 H-Level Input & V_{3HI} & 5.0 & V \\ Pin3 L-Level Input & V_{3HO} & 3.0 & V \\ Pin4 Output & V_{4(sat.)} & I_{SINK} = 2 mA & 0.7 & V \\ Saturation Voltage & & & & & & & & \\ [+5.75V, +5.0V Regulators] & & & & & & & & \\ +5.75V Output Voltage & V_{12} & I_{12} = -20 mA & 5.35 & 5.75 & 6.15 & V \\ +5.75 Output Voltage & V_{12Reg} & I_{12} = 5 mA \rightarrow 20 mA & -25 & 25 & mV \\ Regulation & & & & & & & & & \\ +5.0V Output Voltage & V_{9} & I_{9} = -20 mA & 4.6 & 5.0 & 5.4 & V \\ +5.0V Output Voltage & V_{9} & I_{9} = 5 mA \rightarrow 20 mA & 5.0 & 100 & mV \\ Regulation & & & & & & & & \\ I_{15} & & & & & & & & & \\ I_{15} & & & & & & & & & \\ I_{15} & & & & & & & & & \\ I_{15} & & & & & & & & & \\ I_{17} max & & & & & & & & & \\ Output Voltage & & & & & & & & & \\ V_{10} m_1 m_{10} & & & & & & & & \\ Maximum & & & & & & & & & & & \\ Maximum & & & & & & & & & & & \\ Minimum Operating & & & & & & & & & & \\ Minimum Operating & & & & & & & & & & & \\ Minimum Operating & & & & & & & & & & \\ Minimum Operating & & & & & & & & & & \\ Minimum Operating & & & & & & & & & & & \\ Minimum Operating & & & & & & & & & & & \\ Minimum Operating & & & & & & & & & & & \\ Minimum Operating & & & & & & & & & & & \\ Minimum Operating & & & & & & & & & & & \\ Minimum Operating & & & & & & & & & & & \\ Minimum Operating & & & & & & & & & & & \\ Minimum Operating & & & & & & & & & & & \\ Minimum Operating & & & & & & & & & & & \\ Minimum Operating & & & & & & & & & & & & \\ Minimum Operating & & & & & & & & & & & \\ Minimum Operating & & & & & & & & & & & \\ Minimum Operating & & & & & & & & & & & \\ Minimum Operating & & & & & & & & & & & & \\ Minimum Operating & & & & & & & & & & & & \\ Minimum Operating & & & & & & & & & & & & \\ Minimum Operating & & & & & & & & & & & & \\ Minimum Operating & & & & & & & & & & & & \\ Minimum Operating & & & & & & & & & & & & \\ Minimum Operating & & & & & & & & & & & & \\ Minimum O$						0.8	
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	[Sync Circuit]			-50			μA
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$				0.4	0.72	1.5	V
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		$V_{2(\mathrm{sat.})}$	$I_{SINK} = 10 \text{mA}$			1.0	V
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$							
Pin4 Output Saturation Voltage [+5.75V, +5.0V Regulators] +5.75V Output Voltage V12 I12 = -20mA 5.35 5.75 6.15 V +5.75 Output Voltage V12Reg I12 = 5mA → 20mA -25 25 mV Regulation +5.0V Output Voltage V9 I9 = -20mA 4.6 5.0 5.4 V +5.0V Output Voltage V9Reg I9 = 5mA → 20mA 50 100 mV Regulation [31V Current Source] Pin15 Output Current I15 4.2 6.0 7.8 mA [AFT Shift Circuit] DC Shift Voltage V16 − V17 4.23 4.73 5.23 V Pin17 Maximum V17 max 5.35 5.75 6.15 V Output Voltage [Comparator] Maximum Operating V8 to 10 max 9.0 V Input Voltage Minimum Operating V8 to 10 min 0.7 V Input Voltage Minimum Operating V8 to 10 min 0.7 V Input Voltage Minimum Operating V8 to 10 min 0.7 V Input Voltage				5.0			
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$							
		$V_{4(\text{sat.})}$	$I_{SINK} = 2mA$			0.7	V
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	—						
$ \begin{array}{cccccccccccccccccccccccccccccccccccc$		V	I 90 A	F 0 F	~ = -	0.15	
$ \begin{array}{cccccccccccccccccccccccccccccccccccc$					5.75		•
$ \begin{array}{cccccccccccccccccccccccccccccccccccc$		V 12Reg	112-5mA→20mA	– 25		25	m v
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	•	V _o	I_0 = _20m A	10	۲.0	E 1	7.7
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$			· ·	4.0			
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$		· skeg	ig—omit /20mm		อบ	100	III V
$\begin{array}{cccccccccccccccccccccccccccccccccccc$							
$[AFT Shift Circuit] \\ DC Shift Voltage & V_{16}-V_{17} & 4.23 & 4.73 & 5.23 & V \\ Pin17 Maximum & V_{17} max & 5.35 & 5.75 & 6.15 & V \\ Output Voltage \\ [Comparator] \\ Maximum Operating & V_{8 to 10} max & 9.0 & V \\ Input Voltage & V_{8 to 10} min & 0.7 & V \\ Input Voltage & V_{8 to $		I ₁₅		4.2	6.0	7.8	mΔ
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	[AFT Shift Circuit]	10		2.2	0.0	1.0	1117.5
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	DC Shift Voltage	$V_{16} - V_{17}$	· 7	4.23	4.73	5.23	v
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	Pin17 Maximum						v
$\begin{array}{cccccccccccccccccccccccccccccccccccc$							
Input Voltage Minimum Operating V8 to 10 min 0.7 V Input Voltage							
Minimum Operating V _{8 to 10} min 0.7 V Input Voltage		V _{8 to 10} m	ax	9.0			V
Input Voltage	Minimum Operating	V _{8 to 10} m	in			0.7	v
Output Saturation Voltage V_{11} (sat.) $I_{SINK} = 2mA$ 0.7 V							•
	Output Saturation Voltage	V ₁₁ (sat.)	$I_{SINK} = 2mA$			0.7	V

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